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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

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APPL NUM 10038264	FILING DATE 01/03/2002	CLASS 403	SUBCLASS 270	GAU 3679	EXAMINER GARCIA
<b>**APPLICANTS:</b> Park Seungbae; Sathe Sanjeev; Zubelewicz Aleksander;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A DIV OF 09/430,965 11/01/1999 PAT 6,347,901					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO EN999048D	
Verified and Acknowledged Examiners's initials					
TITLE : Solder interconnect techniques					

U.S. DEPT. OF COMMERCE/PAT. & TRADEMARK OFFICE

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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